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Dkt. 501.39619X00/29900825US1

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Atsushi SHIMODA et al.  
 Serial No. : 09/783,604  
 Filed : 15 February 2001  
 For : METHOD FOR ANALYZING CIRCUIT PATTERN DEFECTS AND A SYSTEM THEREOF  
 Art Unit : 2623  
 Examiner : V.M. Kibler  
 Conf. No. : 9403

PETITION FOR EXTENSION OF TIME

**Mail Stop AF**  
 Commissioner for Patents  
 POB 1450  
 Alexandria, Virginia 22313-1450

14 December 2004

Sir:

Applicant respectfully petitions the Commissioner for a two month extension of the shortened statutory period for response set by the Office Action mailed 14 July 2004 in the above-identified application. The requisite fee of \$450 is attached hereto by Credit Card Payment Form PTO-2038. Please charge any actual deficiency of fees due to ATS&K Deposit Account No. 01-2135 (as Order No. 501.39619X00).

Respectfully submitted,

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450.00 OP



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